**Low power design of ripple carry adder**

*ABSTRACT*

This paper will represent the design and implementation of 16 bit ripple carry adder(RCA), using  
three different CMOS topology as static or conventional CMOS, Gate diffusion input(GDI) and Adiabatic technology. Adder is the basic building for all arithmetic operations like addition, subtraction. we have implemented the 16 bit ripple carry adder in BPTM 45nm CMOS technology in LT spice IV.

**TOOLS REQUIRED:**

* DSCH
* MICROWIND